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ABSTRACT OF THE DISCLOSURE

[0044] A capacitor for use within a microelectronic product employs a first capacitor plate layer that includes a first series of horizontally separated and interconnected lines. A capacitor dielectric layer separates the first capacitor plate layer from a second capacitor plate layer. The second capacitor plate layer includes a second series of horizontally separated and interconnected lines horizontally interdigitated with the first series of horizontally separated and interconnected lines. The capacitor is formed employing a self-aligned method and the capacitor dielectric layer is formed in a serpentine shape.